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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	ATTORNEY DOCKET NO. CONFIRMATION NO.	
10/829,559	04/22/2004	Asher Hazanchuk	ALT.P030 (A1252)	6357	
27296 LAWRENCE	7590 10/21/200 M. CHO	9	EXAMINER		
P.O. BOX 214		DO, CHAT C			
CHAMPAIGN	i, il. 61825		ART UNIT	PAPER NUMBER	
			2193		
			MAIL DATE	DELIVERY MODE	
			10/21/2009	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

## **Advisory Action** Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/829,559	HAZANCHUK ET AL.	
Examiner	Art Unit	
Chat C. Do	2193	

	Chat C. Do	2193					
The MAILING DATE of this communication appe	ars on the cover sheet with the o	orrespondence add	ress				
THE REPLY FILED 10 September 2009 FAILS TO PLACE THI	S APPLICATION IN CONDITION F	OR ALLOWANCE.					
<ol> <li>All The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following application in condition for allowance; (2) a Notice of Appe for Continued Examination (RCE) in compliance with 37 C periods:</li> </ol>	replies: (1) an amendment, affidavi eal (with appeal fee) in compliance	, or other evidence, with 37 CFR 41.31; or	hich places the (3) a Request				
<ul> <li>a) The period for reply expires 3 months from the mailing date</li> </ul>	of the final rejection.						
☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. Examiner Note: (I box 1 is checked, check either box (a) or (b), ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO.							
	MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f). ons of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee						
have been filed is the date for purposes of determining the period of ext under 37 CFF 1.17(a) is calculated from: (1) the expiration date of the set forth in (0) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b).	ension and the corresponding amount of shortened statutory period for reply origing than three months after the mailing date	of the fee. The appropria nally set in the final Office	ate extension fee e action; or (2) as				
NOTICE OF APPEAL	lianes with 27 CED 41 27 must be a	iladithin tua manth.	of the date of				
<ol> <li>The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed w</li> </ol>	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of the					
AMENDMENTS							
<ol> <li>The proposed amendment(s) filed after a final rejection, t         <ul> <li>(a) They raise new issues that would require further cor</li> <li>(b) They raise the issue of new matter (see NOTE belo</li> </ul> </li> </ol>	nsideration and/or search (see NOT		cause				
<ul> <li>(c) They are not deemed to place the application in bet appeal; and/or</li> </ul>	ter form for appeal by materially rec	lucing or simplifying the	ne issues for				
(d) ☐ They present additional claims without canceling a on NOTE: (See 37 CFR 1.116 and 41.33(a)).	corresponding number of finally reje	cted claims.					
4. The amendments are not in compliance with 37 CFR 1.12	21. See attached Notice of Non-Co.	mpliant Amendment (	PTOL-324)				
<ol> <li>Applicant's reply has overcome the following rejection(s):</li> </ol>							
<ol> <li>Newly proposed or amended claim(s) would be all non-allowable claim(s).</li> </ol>	owable if submitted in a separate, t	•					
7.  For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is prov. The status of the claim(s) is (or will be) as follows:		be entered and an e	planation of				
Claim(s) allowed:							
Claim(s) objected to: <u>8 and 10</u> . Claim(s) rejected: <u>1.3.5-7.9 and 11-22</u> . Claim(s) withdrawn from consideration:							
AFFIDAVIT OR OTHER EVIDENCE							
<ol> <li>The affidavit or other evidence filed after a final action, bu because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).</li> </ol>							
<ol> <li>The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to o showing a good and sufficient reasons why it is necessary</li> </ol>	vercome <u>all</u> rejections under appea	l and/or appellant fail:	to provide a				
<ol> <li>The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER</li> </ol>	n of the status of the claims after er	try is below or attach	ed.				
11. X The request for reconsideration has been considered bu	t does NOT place the application in	condition for allowan	ce because:				
See below.  12. Note the attached Information Disclosure Statement(s). (  13. Other:	PTO/SB/08) Paper No(s).						
	101 1 0 5 1						
	/Chat C. Do/ Primary Examiner, Art U	nit 2193					

U.S. Patent and Trademark Office

Part 11: The applicant argues in pages 9-10 for claims that the proposed modification would render the prior art unsatisfactory for its inteded purpose since the secondary reference by Schier clearly states in its summary of the invention.

The examiner respectfully submits that the secondary reference by Schier is applied to only show the missing limitation "the multiplier is on a field programmable gate array and the second product is retrieved from a memory" from the primary reference rather than showing the limitation "scaling the stored value.....from the second number". The limitation "scaling the stored value.....from the second number" is clearly showed by the primary reference Bhandal. The secondary reference only used to show the product of multiplication (e.g., can be previous) can be obtain directly from the look-up table for performance instead of performing direct multiplication to produce the result. Further, nothing in Schier would prevent to be combined with Bhandal from alternatively or additionally shifting after obtaining the product of multiplication.

The applicant further argues in pages 10-12 for claims that there is no motivation as "improving system performance" for combination since there is no need to add a further product from memory and also requiring the stored produt to be added, the multiplier in Bhandal would have its performance worsen not improve as stated by the examiner.

The examiner respectfully submits that it is conventionally known the product LUT is improvement over the direct multiplication (certain degree). By having one of the multiplications replace with direct tookup table to obtain the product would obviously improve over direct multiplication operation.